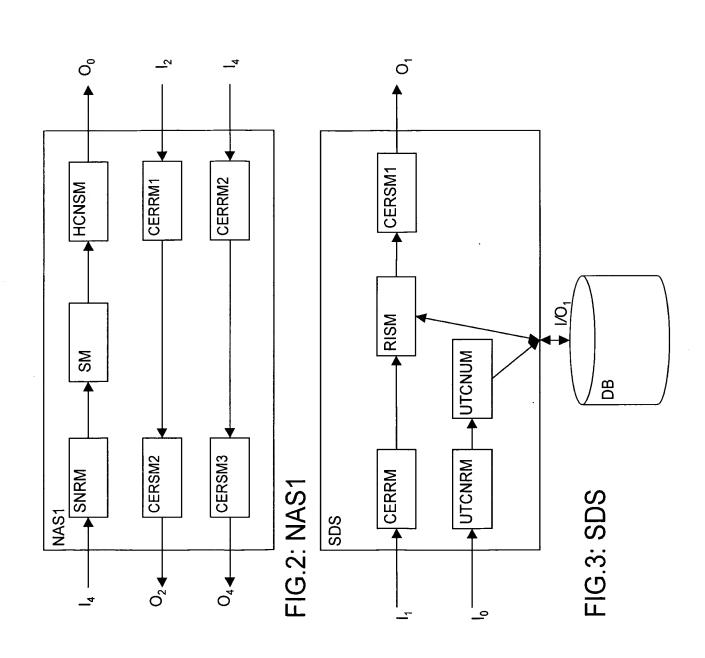


FIG.1: INW



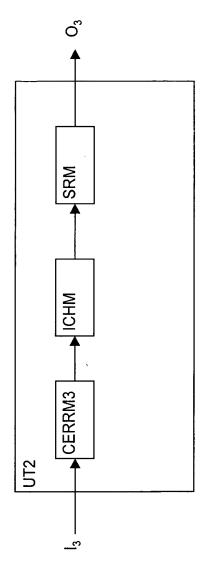


FIG.4: UT2

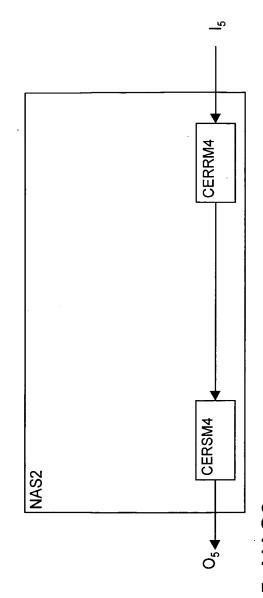


FIG.5: NAS2